VCE Systems Engineering Study Design 2019–2025

Updates effective 1 January 2021

Listed below are amendments to the VCE Systems Engineering Study Design:

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| Page 13 | The ‘NAND’ logic gate diagram in the Cross-study specifications has been updated |

**Additional information:**

* For Unit 3 and 4, students do not need to know logic gates, but will need to demonstrate an understanding of logic statements as they are applied and utilised in coding. Students also need to know the commonly used symbolic representation of the electrotechnological components as listed in the key knowledge sections of the study design.